

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



Re Application of: )  
Peer SCHMITT ) Group Art Unit: 2827  
Serial No.: 10/791,834 ) Examiner: Thong Quoc Le  
Filed: March 4, 2004 ) Attorney Docket No. 003921.00148  
For: TERNARY BIT LINE SIGNALING )

**REQUEST FOR RECONSIDERATION**

U.S. Patent and Trademark Office  
Customer Service Window – Mail Stop Amendment  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir:

This paper is responsive to the non-final Office Action mailed August 1, 2005. Please charge any fees due to our Deposit Account No. 19-0733. Any extensions of time needed for acceptance of this paper are hereby petitioned for. Reconsideration and allowance are respectfully requested in view of the following remarks.

Applicants note with appreciation the Examiner's indication that claims 2-5 would be allowable if rewritten in independent form. However, Applicants believe that the remaining claims are also allowable for at least the reasons stated below.

Claims 1 and 6-9 are rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,319,590 to Montoye ("Montoye"). Applicants respectfully traverse all rejections.

***Independent Claim 1***

Claim 1 recites a memory device, comprising a plurality of addressable memory cells, each memory cell configured to store a first bit and a second bit. Each claimed memory cell includes, among other features, a first storage circuit configured to store the first bit, and a second storage circuit configured to store the second bit. Claim 1 additionally recites that the